



PATENT
20059/PIA31075

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant: Bo Yeoun Jo

Serial No.: 10/750,246

Filed: 12/31/2003

For: Methods of Manufacturing
Semiconductor Devices Having Capacitors

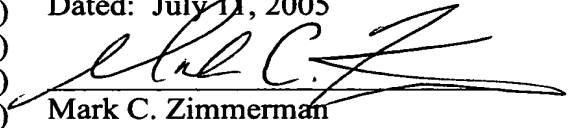
Group Art Unit: 2812

Examiner: Chandra Chaudhari

) I hereby certify that this paper (and/or
) fee) is being deposited with the United
) States Postal Service as first class mail
) in an envelope addressed to:

) Mail Stop Amendment
) Commissioner for Patents
) P.O. Box 1450
) Alexandria, VA 22313-1450

) Dated: July 11, 2005

) 
) Mark C. Zimmerman
) Reg. No. 44,006
)

RESPONSE TO THE OFFICE ACTION DATED FEBRUARY 10, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please enter the following amendments and consider the following remarks.

The Status of the Claims is reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

Amended Drawings are attached.